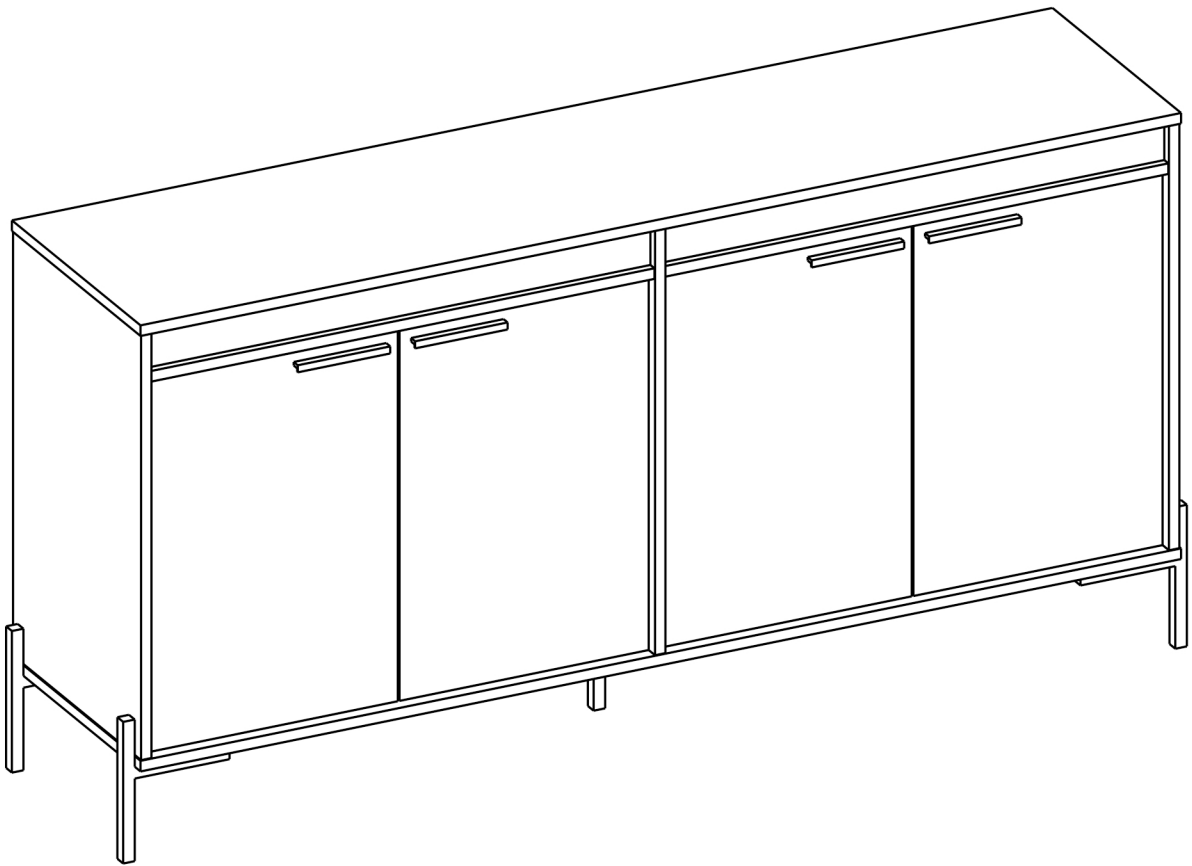


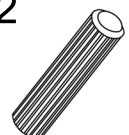
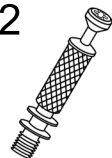



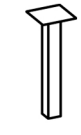
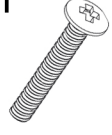


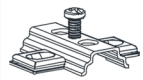
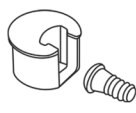
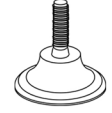


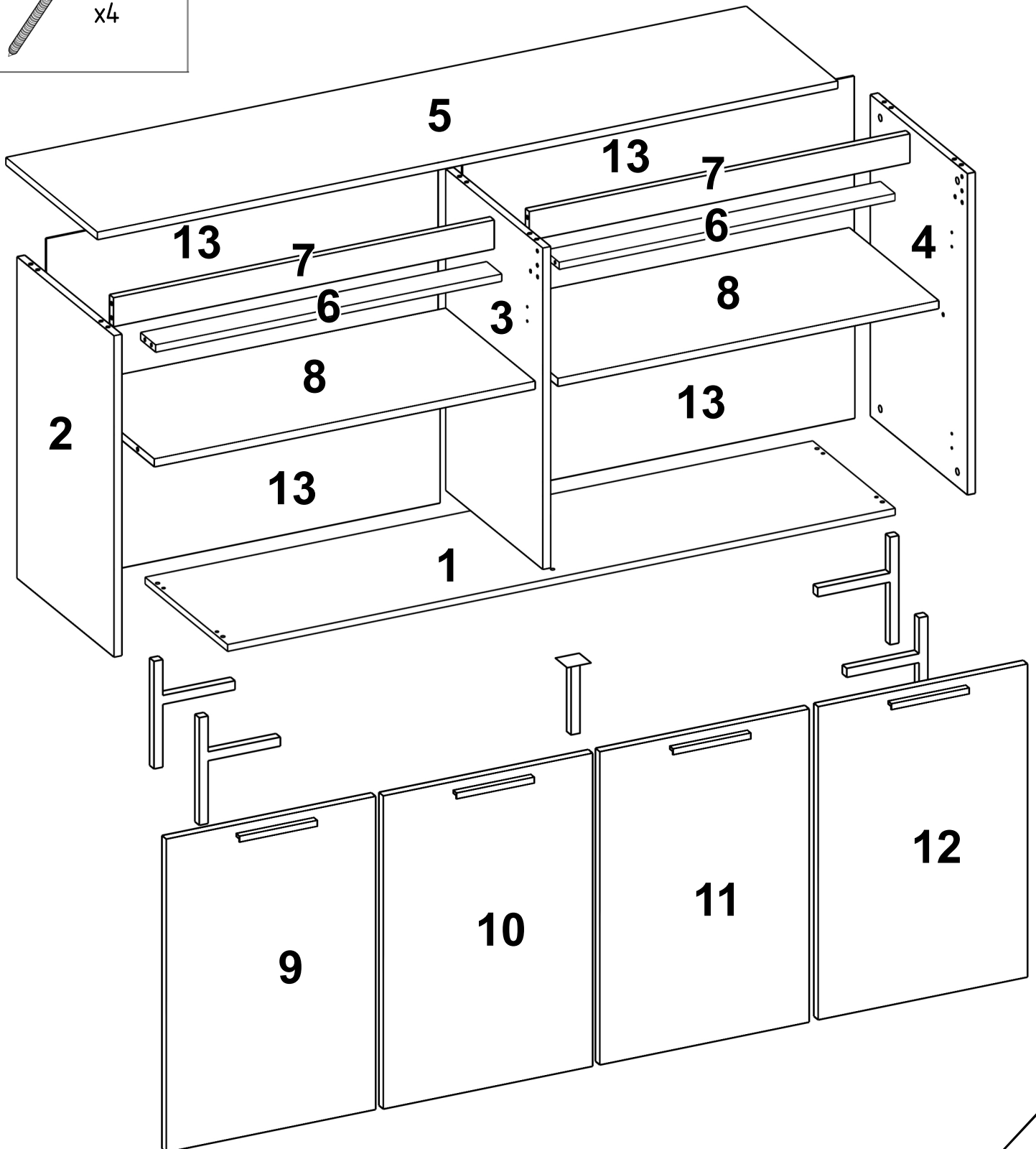
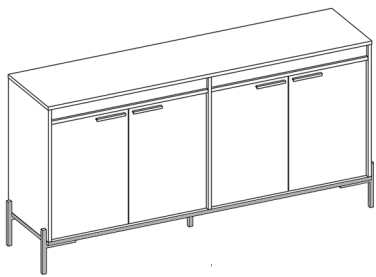


VS5-CGA

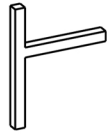


2  B0501 x16	2  B0502 x16	2  B0503 x32	2  B0504 x16	1  B0201 x4
1  B0307 x8	P  D0155 x4	P  D0156 x1	1  B0324 x12	1  B0301 x37
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1  B0304 x4				

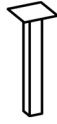




D0155 D0156 B0301 B0324



x4



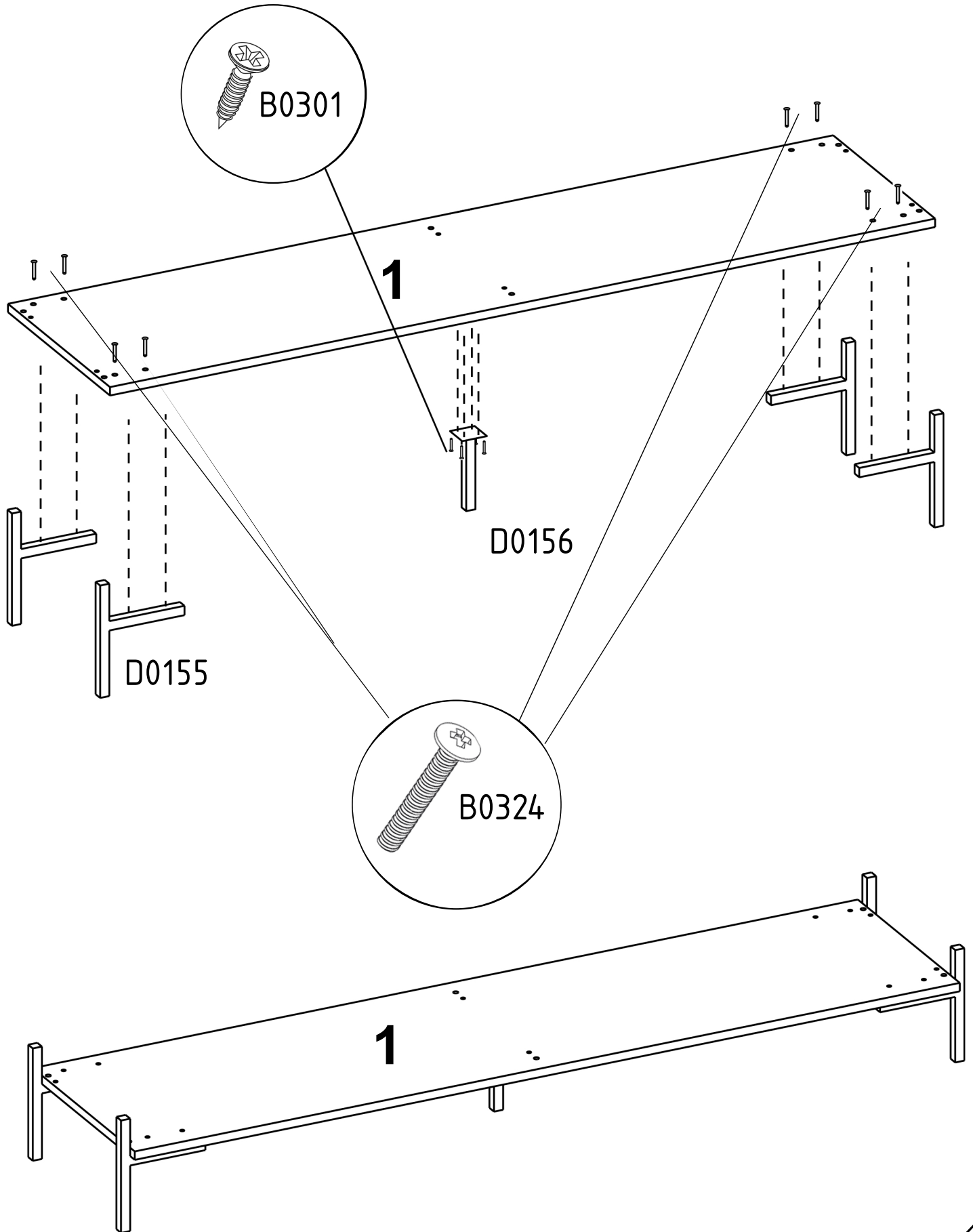
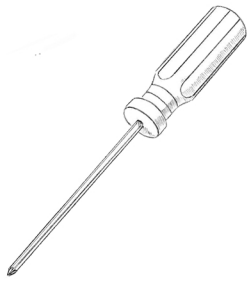
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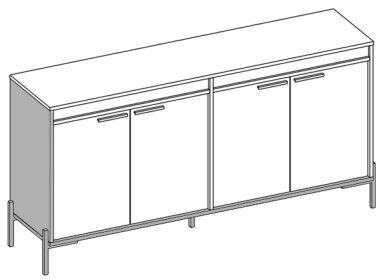


x4



x8





B0502 B0501 B0503 B0504 B0324



x2



x2



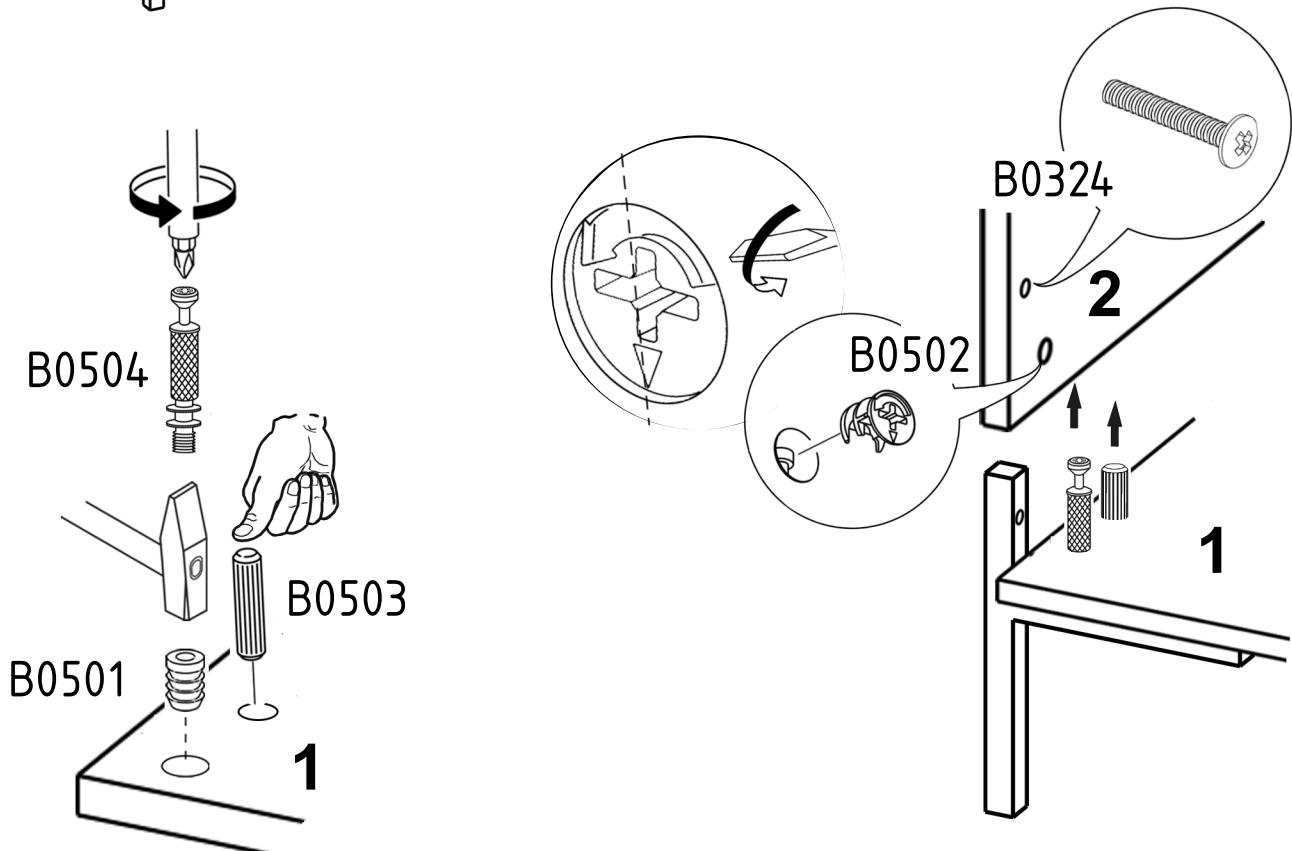
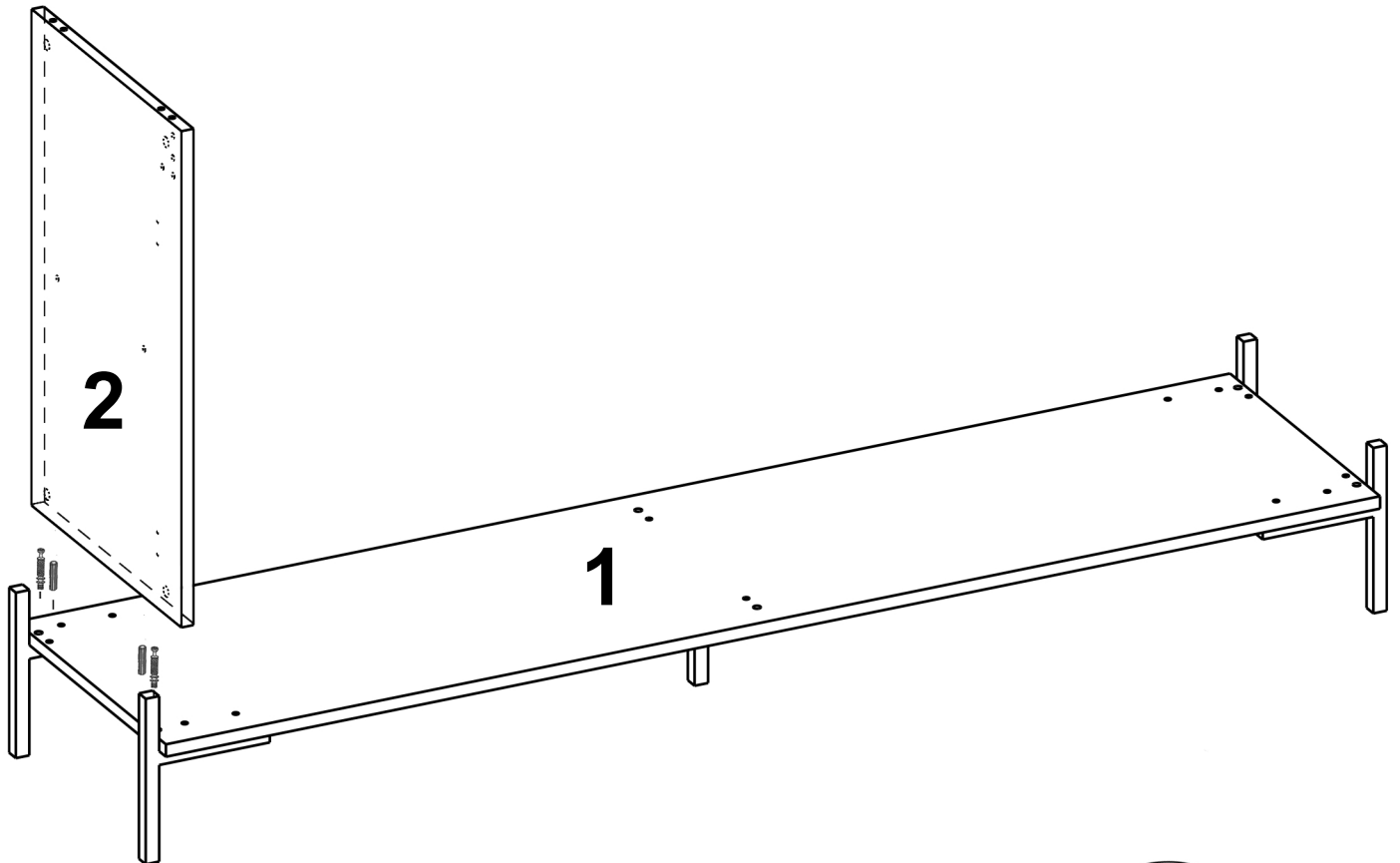
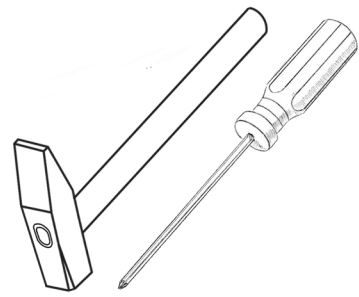
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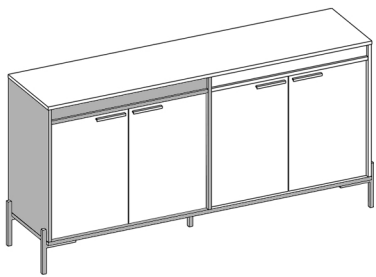


x2



x2





B0502 B0501 B0503 B0504 B0304



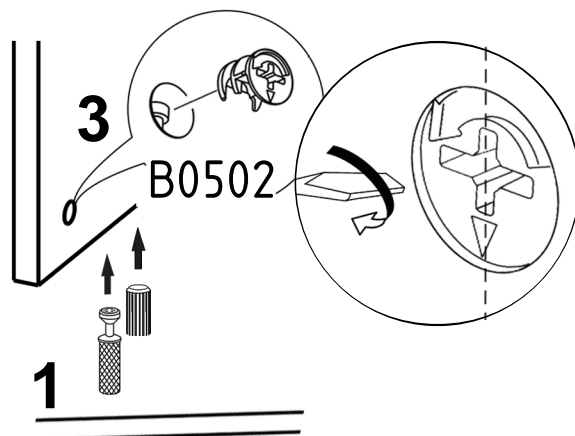
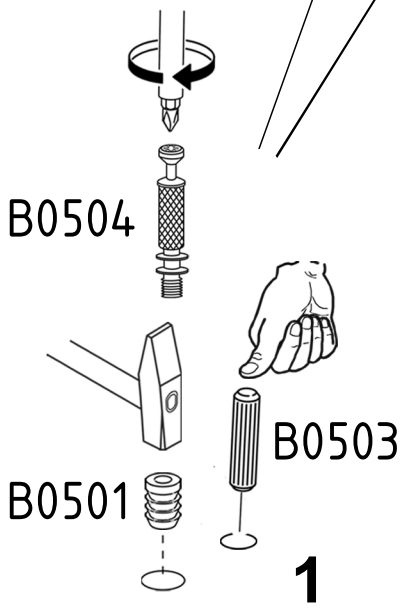
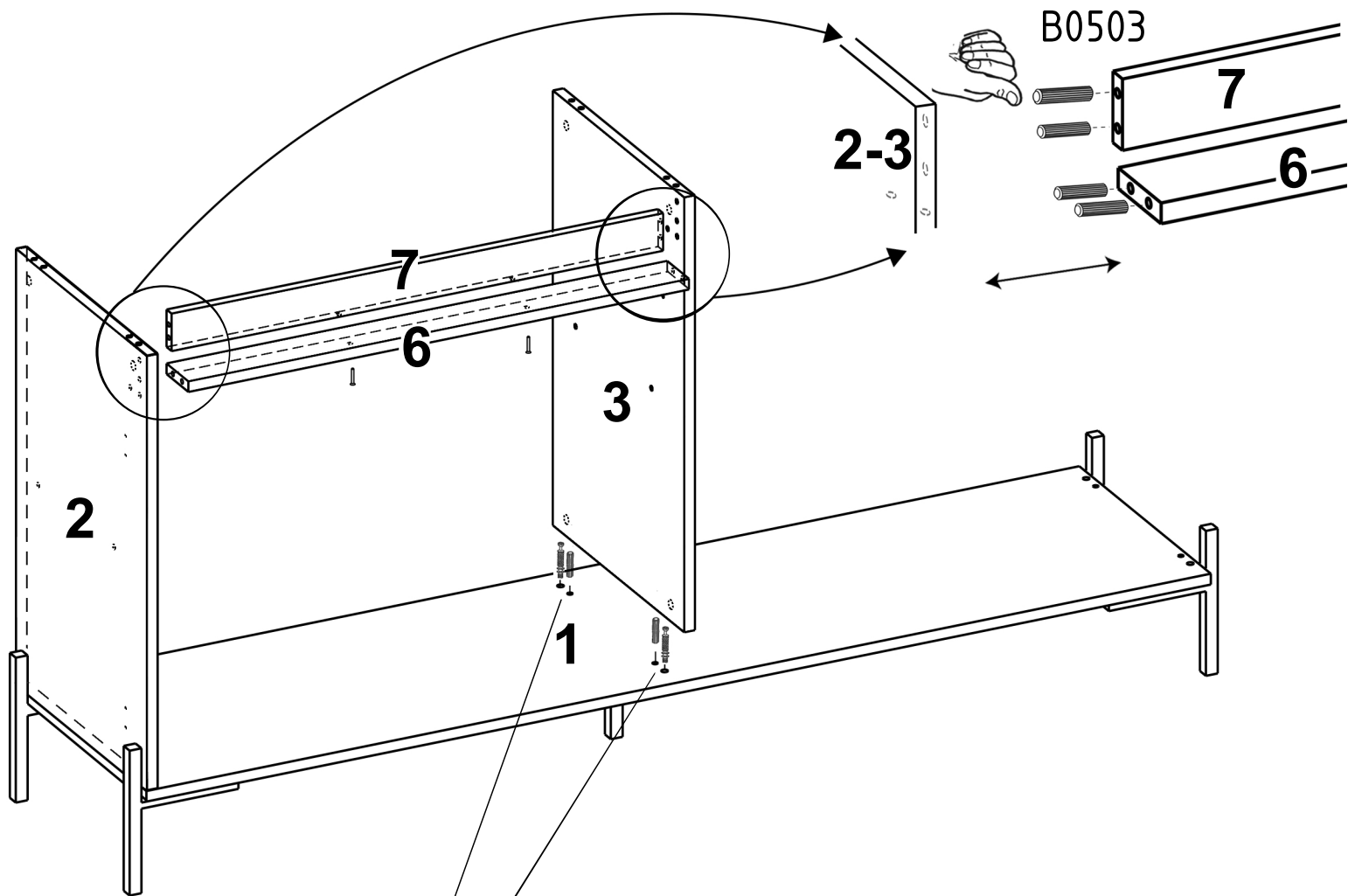
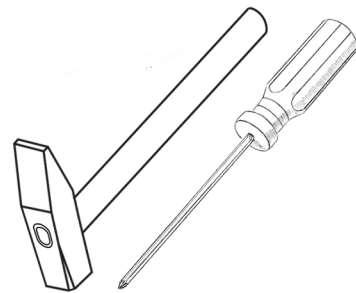
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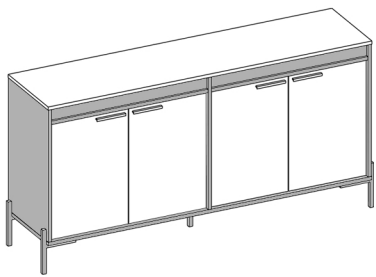
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x10

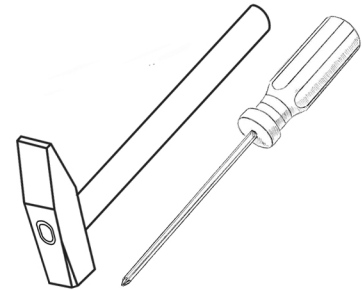
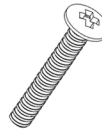
x2

x2

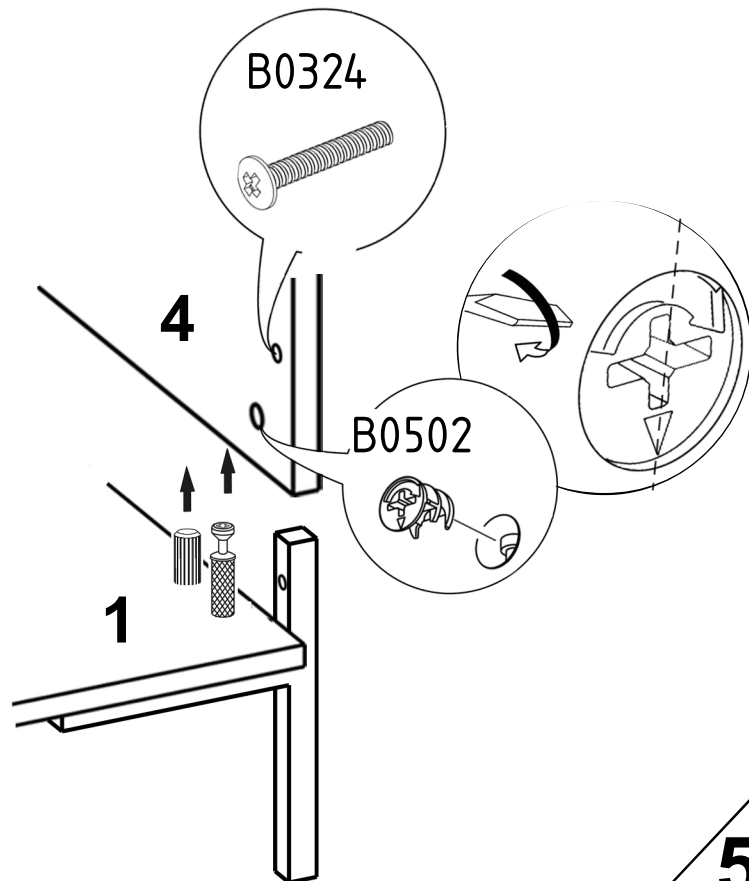
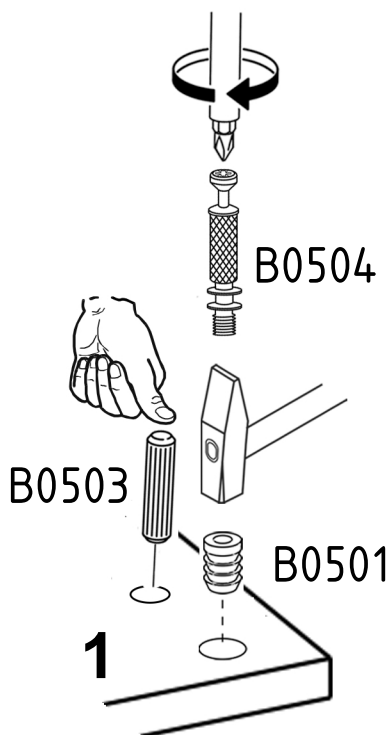
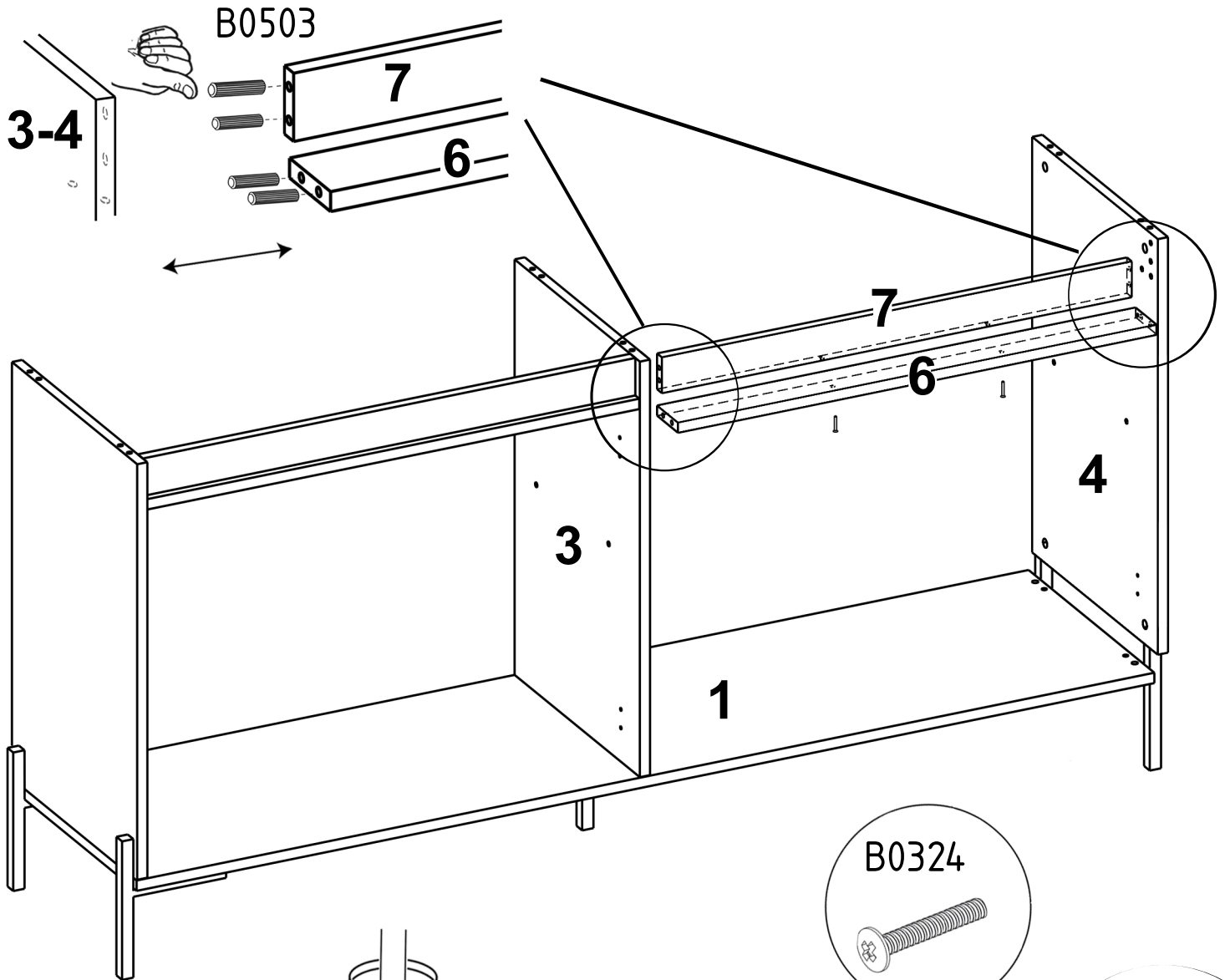


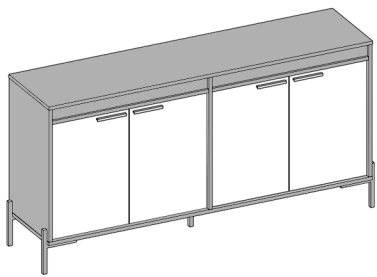


B0502 B0501 B0503 B0504 B0324 B0304

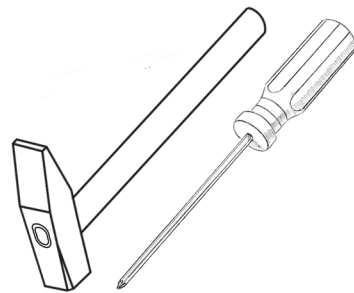


x2 x2 x10 x2 x2 x2

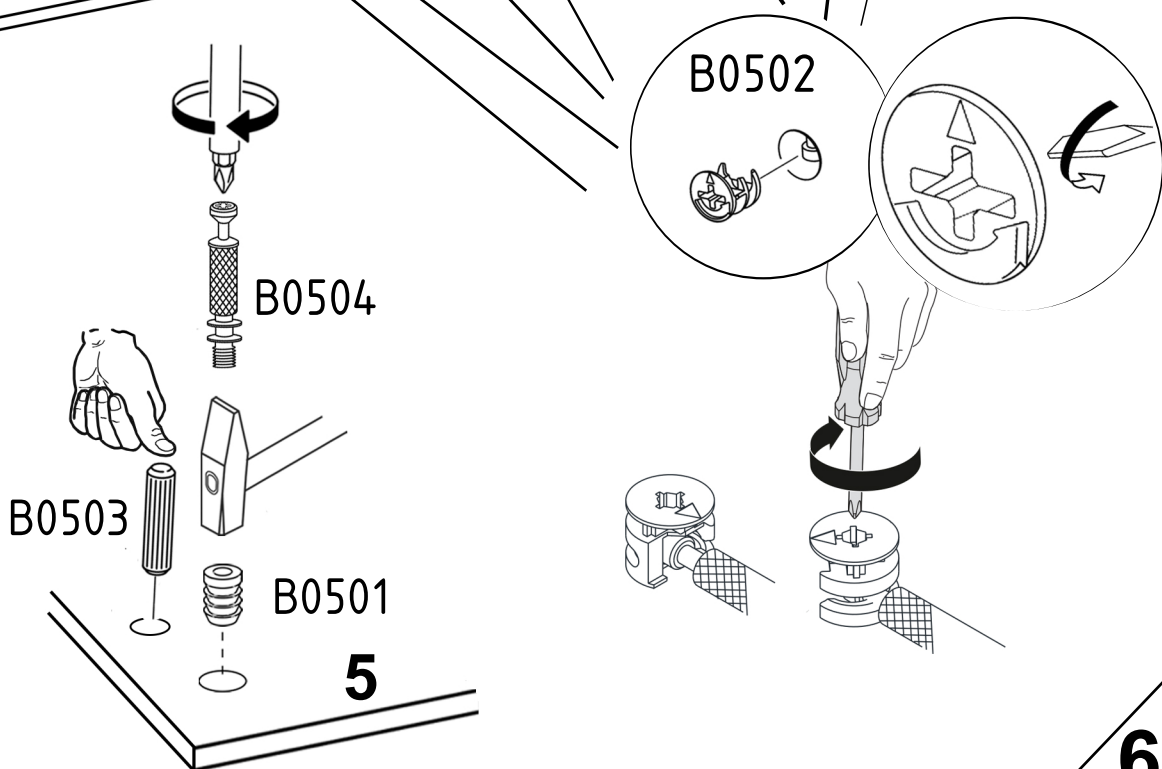
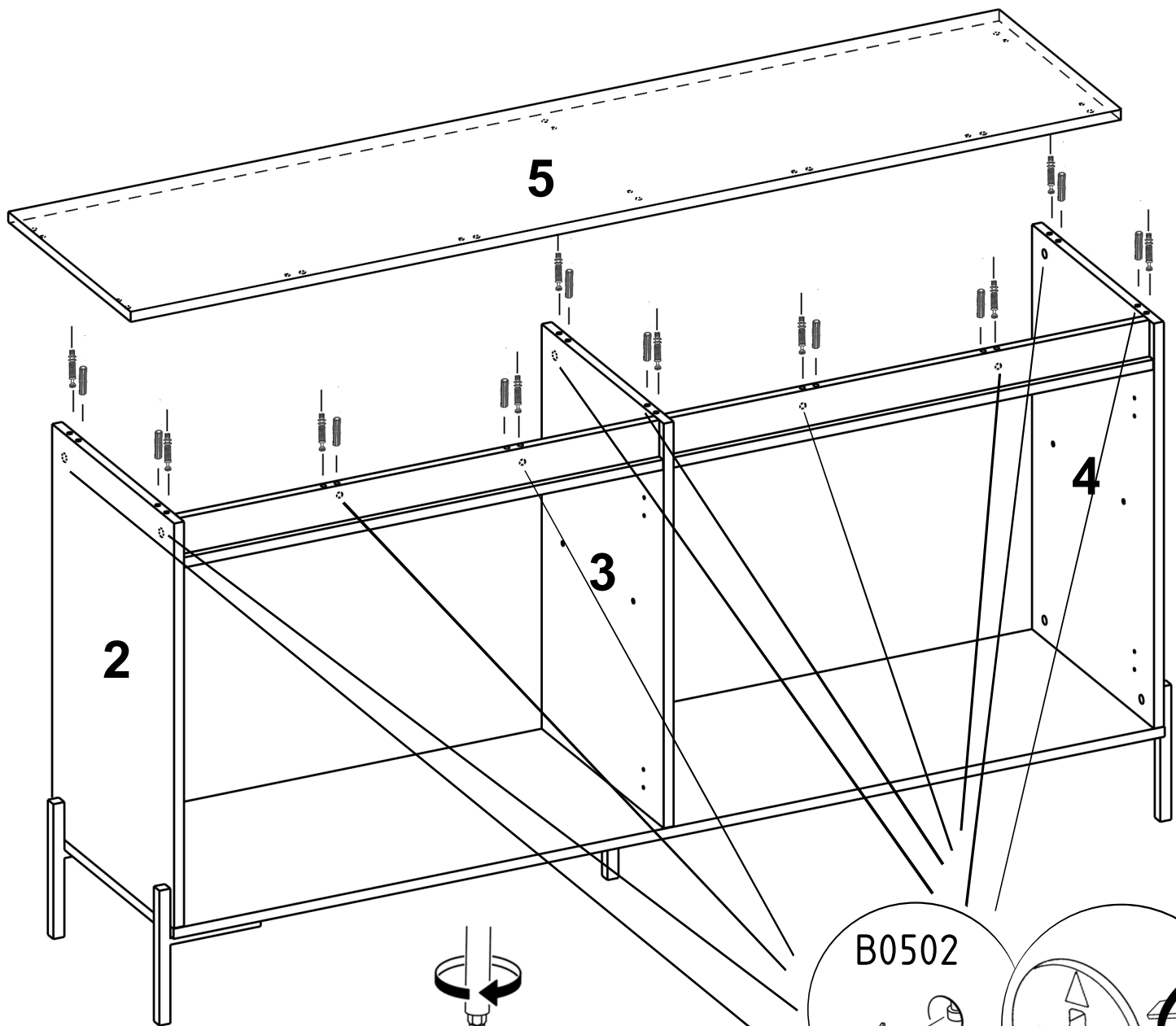


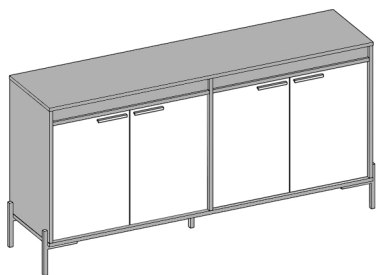


B0502 B0501 B0503 B0504

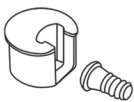


x10 x10 x10 x10

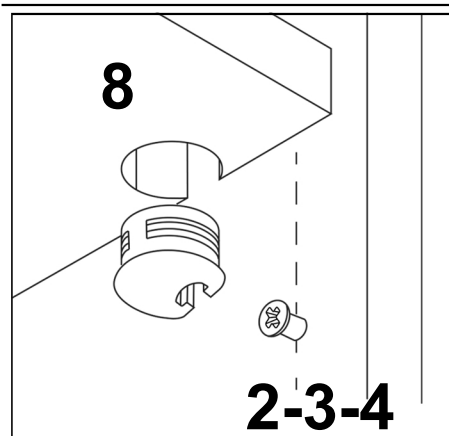
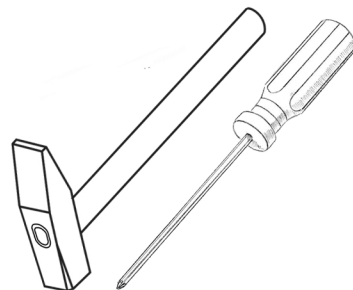




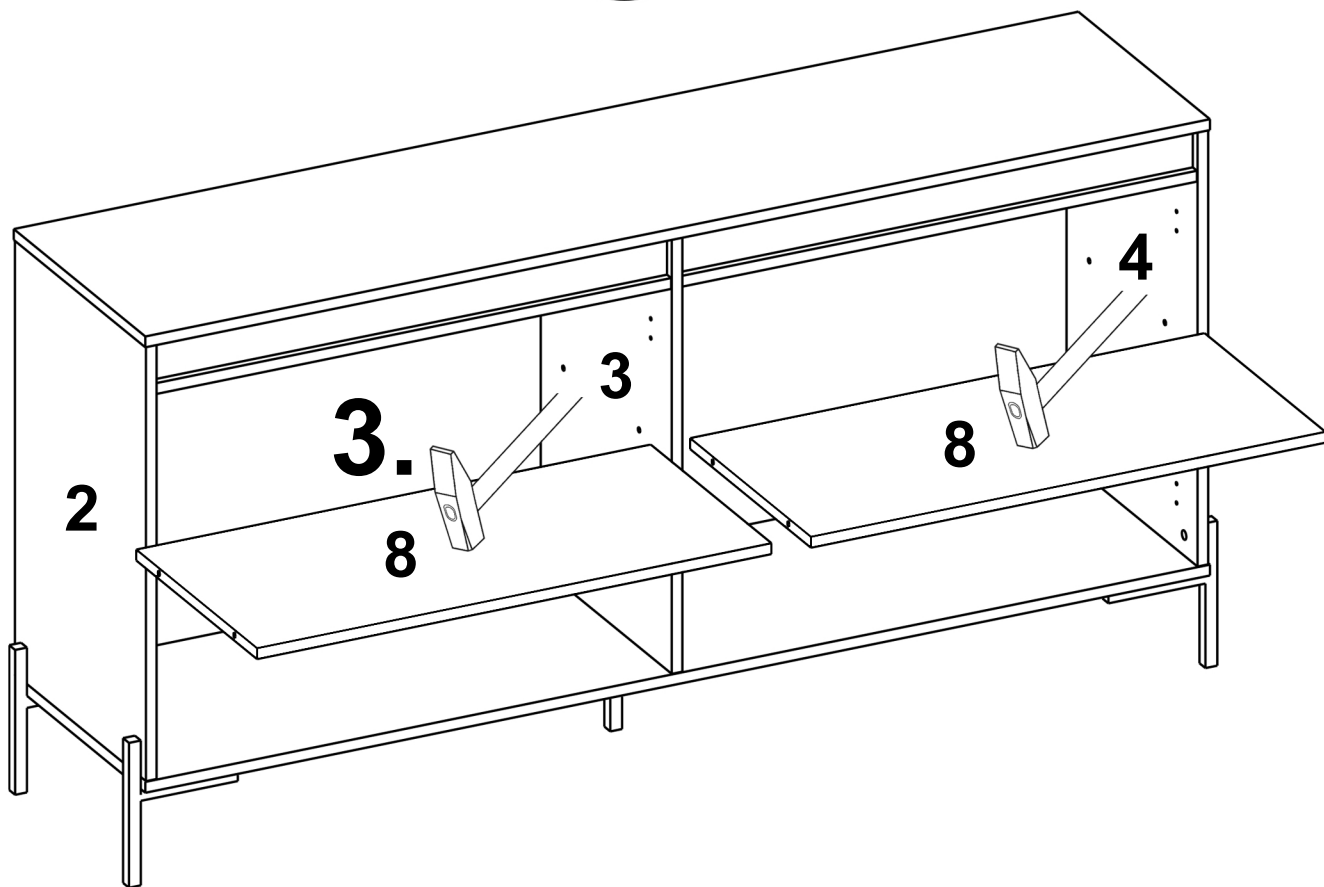
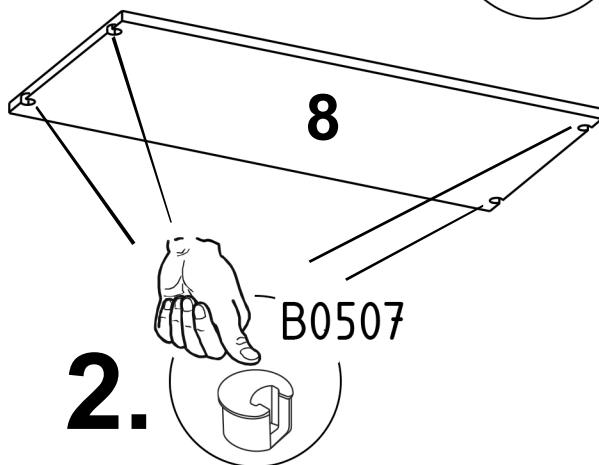
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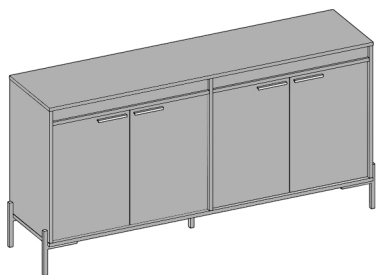


x8



1.





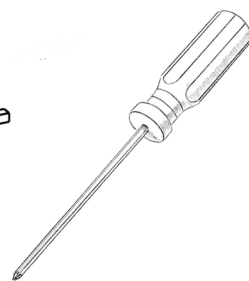
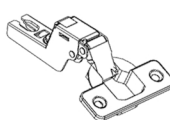
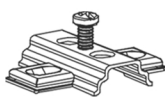
B0301

B0105

B0103

B0307

B0201



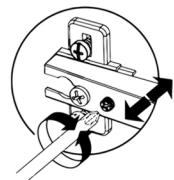
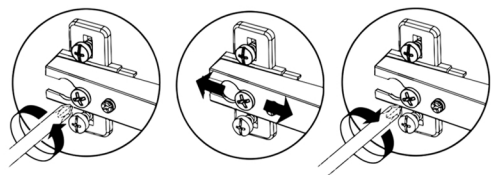
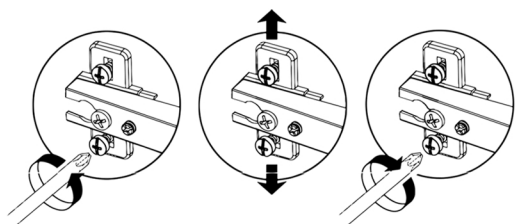
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x8

x8

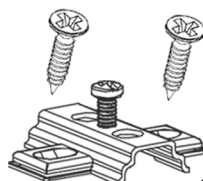
x8

x4



B0105

B0301



B0307

B0201

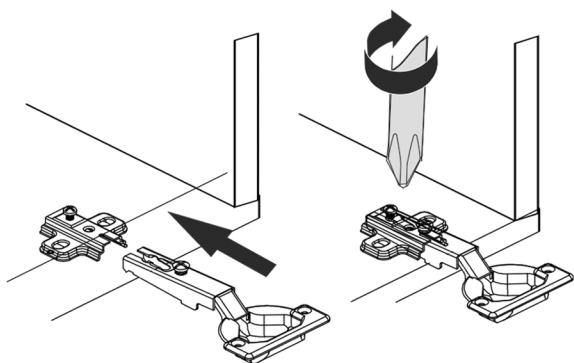
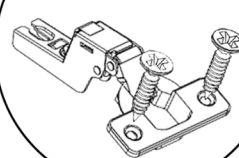
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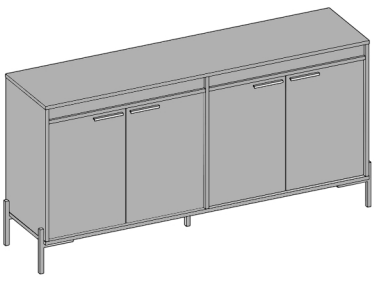
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3

B0301

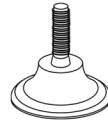
B0103





B0318

B1702



x35

x5

